

図1 試験機全体図

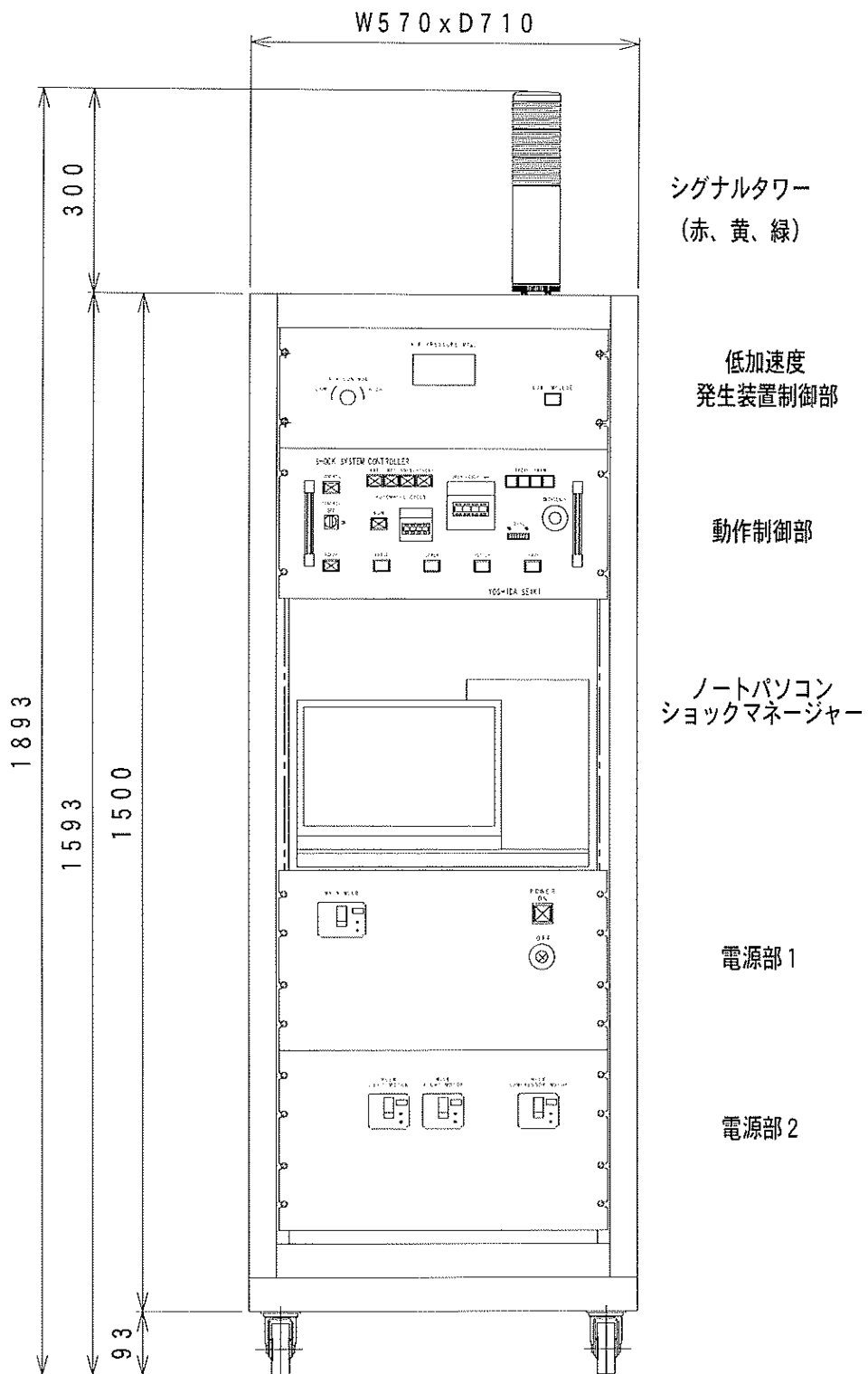


図 2 制御装置